



FACULTY DEVELOPMENT PROGRAMME (FDP) ON

Mixed VLSI System Design using CADENCE Tools
(June 11-16,2018)

Organized at

Rajeev Gandhi Memorial College of Engineering & Technology
Nandyal -518501, Kurnool Dist., A.P.

Sponsored by Electronics & ICT Academy, National Institute of Technology, Warangal
(An Initiative of Ministry of Electronics and Information Technology (MeitY) GOI)



Estd : 1995

Preamble:

“Electronics & ICT Academy” was set up at NIT Warangal with financial assistance from MeitY, GOI. The jurisdiction of this academy is Telangana, Andhra Pradesh, Karnataka, Goa states, Puducherry and Andaman & Nicobar Islands UTs. Role of the academy is to offer faculty development programmes in standard courses and emerging areas of Electronics, Information Communication Technologies, Training & Consultancy Services for Industry, Curriculum development for Industry, CEP for working professionals, advice and support for technical incubation and entrepreneurial activities.

A Faculty Development Programme (FDP) on “MIXED VLSI SYSTEM DESIGN USING CADENCE TOOLS” is being organized by the E & ICT Academy at Rajeev Gandhi Memorial College of Engineering & Technology which has agreed to host the same in its campus by bringing and caring the participants to this programme and also extending its infrastructure for organizing the course involving actively their faculty of ECE Department.

Broad Topics to be covered in FDP:

- Introduction to VLSI System
- Low Power VLSI System Design Techniques
- Introduction to Layout
- **Design of Analog MOS Circuits**
 - Introduction
 - MOSFET Device Characterization
 - CMOS Current Mirrors
 - Single Stage Amplifiers - CS,CD & CG Configuration
 - Design of Differential Amplifier
 - Design of 2-stage CMOS Op-Amp
 - Design of Phase Locked Loop
 - Case Studies
- **Design of Digital MOS Circuits**
 - Introduction to Semi-custom ASIC flow
 - Need and Importance of Physical Design Flow
 - Overview of EDA Tools for Semi-custom IC Design Flow
 - Chip and Input / Output Loads
 - Floor planning and Implementation
 - Power planning and Management
 - Timing Analysis
 - Placement and Optimization
- **Hands-on Sessions on Cadence Tools**
 - Virtuoso
 - Encounter

Resource persons:

E&ICT Academy takes the responsibility of organizing lectures by academicians from reputed institutes in India.

Eligibility:

The programme is open to the teachers of Engineering Colleges and other allied disciplines in Andhra Pradesh, Karnataka, Telangana, Goa states, Puducherry, Andaman & Nicobar Islands UTs. Industry personnel working in the concerned /allied discipline can also attend.

Registration fee for Participants:

Faculty and Research Scholars from Andhra Pradesh, Karnataka, Telangana, Goa states and Puducherry, Andaman & Nicobar Islands UTs.	Rs.2,500/-
Faculty of SC/ST Category from Andhra Pradesh, Karnataka, Telangana, Goa states, Puducherry, Andaman & Nicobar Islands UTs.	Rs.1,250 (SC/ST participants should submit the copy of their caste certificate to claim the concession along with application form)
Industry Participants	Rs.7,500/-
Participants from other states	Rs.8,500/-

SC/ST concession is only for faculty of mentioned states. Research scholars are not eligible for SC/ST concession.

The entire registration fee is to be collected in the form of DDs/online transfer using the following details:

DD Details	Online Transfer details
Demand draft in favor of “Director, NIT Warangal” payable at any bank in Warangal.	Account Name: Electronics & ICT Academy NITW Account No.: 62423775910 IFSC: SBIN0020149

Accommodation:

Free Accommodation will be provided for the outstation participants by the host Institute on prior request.

How to Apply:

A filled -in form of application in the prescribed format duly signed and sponsored by appropriate authorities (along with DD/NEFT Receipt) should reach the coordinator at the host institute by speed post. It is also mandatory to send a scanned application form and DD/NEFT receipt through e-mail to nfouziasulthana@rgmcet.edu.in as selection will be intimated only through e-mail.

Selection Criteria:

Selection will be done based on first-cum-first-serve basis and the confirmed candidates will be notified immediately. The maximum number of participants will be 50 (fifty). Additionally, 10 participants from industry are allowed to participate. The list of selected participants will be sent to their personal Email-Ids. In case of a candidate is not selected, the demand draft will be sent back. A test will be conducted at the end of the course. Candidates will be issued certificates on successful completion of the course along with grade. Reservations are followed for selecting candidates as per GOI norms.

Important Dates:

Last date for submission of application: **June 04, 2018.**

Selection-list intimation/display before: **June 06, 2018**

Duration of the programme: **June 11-16, 2018.**

About NIT, Warangal:

National Institute of technology (formerly Regional Engineering College), Warangal is the first among the 17 RECs setup as joint venture of the Government of India and the State Government. Over the years, the college has established itself as a premier institution imparting technical education of a very high standard leading to the B.Tech., degrees in various branches of engineering, M.Tech., and Ph.D programs in various specializations. All B.Tech., and M.Tech., programs of NIT Warangal are NBA accredited.

About RGM College of Engineering & Technology:

The college was established in 1995 and is situated 10Km away from Nandyal on NH-40 (old 18) towards Kurnool. It has been setup with the aim to achieve technology excellence through innovation and with a motto of” EDUCATION for PEACE”. It is approved by AICTE and accredited by NBA (Tier I), New Delhi, Affiliated to JNTUA, Ananthapuramu, Accredited by NAAC with A+ Grade, New Delhi. The college offers 5 U.G. programs and 7 P.G. programs and possesses very good infrastructure and faculty.

Coordinators:

1. **Dr. P.Sreehari Rao, Dept., of ECE,**

NIT Warangal

Email Id: patri@nitw.ac.in

Ph. No. 9441342324

2. **N.Fouzia Sulthana, Dept., of ECE,**

Rajeev Gandhi Memorial College of Engineering & Technology,
Nandyal

Email Id: nfouziasulthana@rgmcet.edu.in

Ph. No. 9059470004/9299450420



**Ministry of Electronics and Information Technology (MeitY), GOI sponsored
FACULTY DEVELOPMENT PROGRAMME (FDP) On
Mixed VLSI System Design using CADENCE Tools
(June 11-16,2018)**



**To be organized at
Rajeev Gandhi Memorial College of Engineering & Technology
Nandyal-518501, Kurnool Dist., A.P.
by Electronics & ICT Academy, National Institute of Technology, Warangal**

APPLICATION FORM

- Name of the Applicant:
- Designation:
- Email Id. :
- Category:
 - Faculty/Research Scholars from jurisdiction of E&ICT Academy, NIT Warangal (Rs.2,500/-)
 - SC/ST Faculty members (Rs. 1,250/-)
 - Industry Participants (Rs. 7,500/-)
 - Participants from other states (Rs. 8,500/-)
- Institution :
- Mobile No.:
- Payment Mode:
 - Demand Draft No._____
 - NEFT /UTR No. :_____
 - Bank: _____
 - Date: _____
- Educational Qualification with Specialization:
- No. of refresher courses /workshops Attended:
- Experience (in Years): Teaching: Research: Industry:
- Do you belong to reservations SC/ST? (YES/NO):
(If yes, please specify and attach a copy of caste certificate to claim the concession)
- Do you require accommodation during Training? : YES/NO

Declaration

The information provided is true to the best of my knowledge. If selected, I agree to abide by the rules and regulations of the FDP and shall attend the course for the entire duration. I also undertake the responsibility to inform the Coordinator in case, I am unable to attend the course.

Place :

Date :

Signature of the Applicant

Sponsorship Certificate

Dr./Mr./Ms. is an employee of our Institute/Organization and is hereby sponsored to participate in the FDP on "Mixed VLSI System Design Using Cadence Tools" Sponsored by Electronics & ICT Academy during June 11-16, 2018 at Rajeev Gandhi Memorial College of Engineering & Technology, Nandyal, Kurnool Dist., AP.

Place:

Date:

Signature of Principal/Head of the Institution

Address for Correspondence:

Post your application form along with DD/NEFT receipt to

N.Fouzia Sulthana,
Assistant Professor,
Department of ECE,
Rajeev Gandhi Memorial College of Engineering & Technology,
Nerawada 'X' Roads, NH-40,
Nandyal, Kurnool District,
Andhra Pradesh, Pin code -518501.
Mobile No. 9059470004/9299450420
Email Id: nfouziasulthana@rgmcet.edu.in

NOTE

Mail the scanned copies of filled-in and duly signed application form with DD/NEFT receipt to nfouziasulthana@rgmcet.edu.in

For more details about Electronics and ICT Academy, please visit: <http://nitw.ac.in/eict/>. For more details, about RGM College of Engineering & Technology, Nandyal, Please visit: <http://rgmcet.edu.in> .