

CHIEF PATRON:

**Dr. M. Santhi Ramudu**

Chairman, RGM Group of Institutions

PATRON:

**Dr. T. Jayachandra Prasad**

Principal, RGM CET

**Sri. M. Shivaram**

Managing Director, RGM CET

CO-PATRON:

**Dr. D. V. Ashok Kumar**

Dean, Administration

**Dr. D. Satyanarayana**

Controller of Examinations

CONVENOR:

**Dr. M. Ramana Reddy**

Head of Department

Co-Convenors:

**Dr. A. Sathish**

Professor

**Dr. P. V. Gopi Krishna Rao**

Professor

CO-ORDINATORS:

**Mr. C. Venkataiah**

Assoc.Prof.

**Dr. V. Ramesh Kumar**

Assoc.Prof.

**ORGANIZING COMMITTEE:**

Mr. K. Mallikarjuna

Mrs. J. Sofia Priyadarshini

Mr. V.N.V. Satya Prakash

Mr. P. Rangappa

Mr. Y. Madhusudhana Reddy

Mr. G. Ramesh

Mr. M. Chennakesavulu

Mr. P. Samulu

Mr. N. Ramanjaneyulu

Mr. S.V. Ratan Kumar

Mr. N. Nagaraja Kumar

Mr. R. Hanuman Naik

**HOW TO APPLY**

Registration may be done by sending filled in application form available in this brochure along with DD to reach the Institute on or before **06-06-2017**

Online registration can also be made by downloading the registration form from college website and sending it along with the scanned copy of Demand Draft to [rgmece2015@gmail.com](mailto:rgmece2015@gmail.com) (or) [venki.challa@gmail.com](mailto:venki.challa@gmail.com)

**REGISTRATION FEE**

For Faculty/Research Scholars : Rs. 1000/-  
PG Students : Rs.750/-

Registration Fee includes Lunch, Tea & snacks during Workshop. Accommodation will be arranged in Nandyal town on payment basis. The Registration Fee Demand draft should be drawn in favour of HOD of ECE, Payable at SBI, RGM CET, Nandyal (IFSC Code No: SBIN0010577), Andhra Pradesh, India.

**CONTACT DETAILS**

**Mr. C. Venkataiah** 95027 52523

**Mr. N. Ramanjaneyulu** 98854 73297

**Mr. M. Chennakesavulu** 99491 88178



**RGM COLLEGE OF  
ENGINEERING & TECHNOLOGY**

(AUTONOMOUS)

Affiliated to JNTUA- Anantapuramu, Approved by AICTE -New Delhi,  
Accredited by N.B.A - New Delhi  
Accredited by NAAC with 'A' Grade, Participated in  
World Bank Assisted TEQIP - I

NERAWADA, 'X' ROADS, NANDYAL-518501  
KURNOOL (DIST), A.P.

ONE WEEK FACULTY DEVELOPMENT  
PROGRAM ON

**ADVANCED VLSI DESIGN  
USING CADENCE TOOLS**

12<sup>th</sup> June - 17<sup>th</sup> June, 2017



Organized by  
**Dept. of Electronics & Communication Engg.**

In Technical Collaboration with  
**Cadence India and Entuple Technologies**

**cadence**

**ENTUPLE  
TECHNOLOGIES**

In Association with



### ABOUT THE INSTITUTE

Rajeev Gandhi Memorial College of Engineering & Technology (RGM CET), Nandyal was established in the year 1995 as the first private engineering college under JNTU. RGM CET is playing a significant role in imparting technological education in the state of Andhra Pradesh. The Institution is offering 06 UG (B. Tech) programmes viz., CE, EEE, ECE, CSE, IT and ME, 07 M. Tech , MCA & MBA Programmes. It also offering Ph.D in 9 Departments.

### ABOUT THE DEPARTMENT

The department of ECE offers U.G,P.G & Ph.D courses with an intake of 240, 36 and 02 respectively. It has excellent infrastructural facilities and well equipped laboratories, audio visuals and software tools. Right from the inception, the department works with an objective to provide the best quality technical education at graduate and post graduate levels. In addition to this the department also encourages R&D, consultancy and interaction with industry to achieve academic excellence.

### KEY LEARNING OUTCOMES

*At the end of the program you will be able to*

- Characterize the PDK for device
- Synthesize of basic digital and analog circuit topologies
- Analyze the performance Specification Requirements and identify the suitable circuit topologies
- Carry out the Design for DC and AC Performance
- Derive Layout Constraints for the Physical Design and generate DRC and LVS for Physical Design.
- Carry out the Physical Verification and Parasitic Extraction Back Annotate and carry out post Layout Simulation/Characterization
- HDL Capturing and Functional Simulation of HDL Codes.
- Synthesis of HDL Designs
- Running Physical Design for the HDL Circuits
- Generating Stream data file

### RESOURCE PERSONS

Various Experts Drawn from Cadence India, M/S Entuple Technologies Pvt Ltd., Bangalore which is the authorized partner of CADENCE & renowned academic institutions.

### REGISTRATION FORM

#### FDP ON ADVANCED VLSI DESIGN USING CADENCE TOOLS

12<sup>th</sup> June - 17<sup>th</sup> June, 2017

Name :

Designation :

Organization :

Address for Communication:

Contact No.:

E-mail :

Registration Fee :

DD No. & Date :

Issuing Bank :

Place :

Date :

Signature of Applicant

College Seal

Signature of the  
Head of Institution