

**RAJEEV GANDHI MEMORIAL COLLEGE OF ENGINEERING & TECHNOLOGY**

**Autonomous**

**Department of Electronics and Communication Engineering**

**OTHER INTERNATIONAL JOURNALS**

**Academic year :2024-25 (July-2024 to June-25)**

1. **S. Govindarajulu**, B.Anusha, K. Ravi Kumar, S. Raheem Basha, V. Karthik, "Implementation of fast, power and energy efficient CMOS flip flop in 45 nm regime," Communications on Applied Nonlinear Analysis, vol. 32, no. 10, May 2025. <https://internationalpubs.com/index.php/cana/article/view/5248>
2. **S. Govindarajulu**, K. Sudheer, V. E. Manideep, E. S. Kumar, and K. Kiran, "Low power and energy efficiency techniques for full adder – Implementation, comparison and analysis in 45 nm technology," Power System Technology, vol. 49, no. 2, May 2025. <https://powertechjournal.com/index.php/journal/article/view/1788/1310>