

# R G M COLLEGE OF ENGINEERING & TECHNOLOGY:: Nandyal AUTONOMOUS

**Ms.N.FOUZIA SULTHANA**

Assistant Professor,  
Electronics and Communication Engineering



Date of Birth: 12-07-1988

Date of Joining: 23-07-2010

Experience: 7 Years 2 Months

Email: fouzia.nfs@gmail.com

Phone: 09299450420

## Qualifications: (Start with highest degree)

Examination Passed	Class	% of marks	Year	University/Institution
Ph.D.	--	--	Pursuing	JNTUA, Ananthapur
M.Tech., [Embedded Systems]	FWD	83.1	2014	RGMCET- JNTUA, Ananthapur
B.Tech., [ECE]	FWD	71.02	2009	RGMCET- JNTUA, Ananthapur

## RESEARCH & PUBLICATIONS

- Text Books - 00
- Book Chapter - 00
- International Journals - 00
- International Conferences - 02
- National Journals - 00
- Conferences/Workshops/Symposia's - 13 WORKSHOPS  
11 FDPs  
04 In-House Training Programmes  
03 Short-Term Courses
- Research Funding Projects - 00 Ongoing () 00 Completed ()
- Patents - 00
- Consultancy Works - 00 Ongoing(), 00 Completed()

## MEMBERSHIPS & HONARARY ROLES

Associate Member of "The Institute of Engineers (India) –AMIE"

## Workshops, Symposia, Seminars, Guest lecturers & Conferences organized: 07

- 02-FDPs
- 05 Training Programmes

## Awards & Achievements: 00

NA

## Courses Handled/Handling: 12

- | B.Tech.,                                  | M.Tech.,                              |
|---|---------------------------------------|
| ● Digital Logic Design (01)               | ● Advanced Computer Architecture (02) |
| ● Antennas & Wave Propagation (06)        | ● Embedded 'C' (01)                   |
| ● Microprocessor & Microcontroller (01)   | ● Hardware/Software Co-Design (01)    |
| ● Analog Communications (02)              |                                       |
| ● Radar Systems (04)                      |                                       |
| ● Digital Communications (04)             |                                       |
| ● Digital IC Applications Using VHDL (02) |                                       |
| ● Random Variables & Random Processes     |                                       |
| ● VLSI Design (01)                        |                                       |