R G M COLLEGE OF ENGINEERING & TECHNOLOGY (AUTONOMOUS)

Dr. V. Ramesh Kumar

Associate Professor,

Electronics and Communication Engineering

Date of Birth: 30-June 1985 Date of Joining: 01-April-2016

Experience: 9 Years

Email: rameshkumar.nith@gmail.com Phone:+91-8008409231

Qualifications: (Start with highest degree)

Examination passed	Class	% of marks	Year	University/Institution
PhD	-	-	2016	IIT Roorkee
M.Tech	First	8.02	2010	NIT Hamirpur
B.Tech	First	67.6	2007	Bapatla Engineering College

RESEARCH & PUBLICATIONS

CII & I UDDICATIONS		
Text Books	- 2 (CRC Press, Springer)	
Book Chapter	- 2 (IET, Springer)	
International Journals	- 14 (All in SCI Indexed Journals)	
International Conferences	- 8	
National Journals	- 0	
Conferences/Workshops/Symposia's	- 5	
Research Funding Projects	- SERB-1Ongoing (1) Completed (0)	
Patents	- 0	
Consultancy Works	- 0 Ongoing(0), Completed(0)	
	Text Books Book Chapter International Journals International Conferences National Journals Conferences/Workshops/Symposia's Research Funding Projects	Book Chapter - 2 (IET, Springer) International Journals - 14 (All in SCI Indexed Journals) International Conferences - 8 National Journals - 0 Conferences/Workshops/Symposia's - 5 Research Funding Projects - SERB-1Ongoing (1) Completed (0) Patents - 0

MEMBERSHIPS & HONARARY ROLES

• Member IEEE

Reviewer

- IEEE Transactions on Nanotechnology, Electron Devices, Electromagnetic compatibility,
- Elsevier Microelectronics Journal, Microelectronics Reliability,
- Springer Journal of Computational Electronics, Circuits, Systems, and Signal Processing.

Workshops, Symposia, Seminars, Guest lecturers & Conferences organized:

- Delivered guest lecture at IIIT-Naya Raipur on "Design of Ternary Logic Gates for Speed VLSI"
- Organized Seminars and Workshops at RGMCET on "Advanced Trends in Electronic Circuit Design"

Awards & Achievements:

- Early Carrier Research Award (ECRA), SERB, DST India.
- IEEE Student Travel Grant Award

Courses Handled/Handling:

- Electronic Devices and Circuit
- Switching Theory and Logic Design
- MOS Circuit Design
- Low Power VLSI
- VLSI Design

