

**R G M COLLEGE OF ENGINEERING & TECHNOLOGY**  
**(AUTONOMOUS)**

**Dr. C. VENKATAIAH**

Associate Professor,  
Electronics and Communication Engineering



Date of Birth: 12-06-1985

Date of Joining: 01-07-2009

Experience: 10 Years 4 months

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**Qualifications: (Start with highest degree)**

Examination passed	Class	% of marks	Year	University/Institution
Ph.D. ( VLSI Interconnects)			2018	JNTUK, KAKINADA
M.Tech ( VLSI Design)	1	6.98 CGPI	2009	NIT, HAMIRPUR, H.P
B.Tech (ECE)	1	63.78%	2007	JNTUH, HYDERABAD
Diploma (DECE)	1	66.02%	2004	ESC GOVT. POLYTECHNIC
SSC	1	78.5%	2001	SRKGV GOVT. HIGH SCHOOL.

**RESEARCH & PUBLICATIONS**

- Text Books - 01
- Book Chapter - 01
- International Journals - 12
- International Conferences - 01
- National Journals - 00
- Conferences/Workshops/Symposia's - 02
- Research Funding Projects - NIL Ongoing () Completed ()
- Patents - NIL
- Consultancy Works - NIL Ongoing(), Completed()

**MEMBERSHIPS & HONARARY ROLES**

MIEEE

**Workshops:, Symposia, Seminars:, Guest lecturers & Conferences organized: 03**

**Awards & Achievements:**

**Courses Handled/Handling:**

DIGITAL IC APPLICATIONS, VERY LARGE SCALE INTEGRATION, ELECTRONICS DEVICES AND CIRCUIT, SWITCHING THEORY AND LOGIC DESIGN, VERILOG HDL, FPGA AND CPLD ARCHITECTURES, TESTING AND TESTABILITY, CMOS DIGITAL IC DESIGN, ADVANCED COMPUTER ARCHITECTURE, LOW POWER VLSI DESIGN, MICROPROCESSORS AND MICROCONTROLLERS.