

R G M COLLEGE OF ENGINEERING & TECHNOLOGY (AUTONOMOUS)

Dr. M. CHENNAKESAVULU

Associate Professor,
Electronics and Communication Engineering



Date of Birth: 31.08.1982

Date of Joining: 15.09.2004

Experience: **17** years **1** Month

Email: chennakesava318@gmail.com

Mobile: 9949188178

Qualifications: (Start with highest degree)

Examination passed	Class	% of marks	Year	University/Institution
SSC	FWD	77	1997	SSC
Intermediate (MPC)	FWD	83.1	1999	Board of Intermediate
B.Tech (ECE)	First	66.67	2003	JNTUH
M.Tech(ES)	FWD	83	2010	JNTUA
Ph.D	First	63.5	2020	JNTUA

RESEARCH & PUBLICATIONS

- Text Books - 01
- Book Chapter -
- International Journals - 15
- International Conferences - 03
- National Journals - 01
- Conferences/Workshops/Symposia's - 22
- Research Funding Projects -
- Patents - 01
- Consultancy Works -

MEMBERSHIPS & HONARARY ROLES

- Associate Member of IETE from 2007 ID : AM 191935

Workshops, Symposia, Seminars, Guest lecturers & Conferences organized: 03

Awards & Achievements:

- Qualified in GATE
- Qualified in UGC-NET

Courses Handled/Handling:

B.Tech : Electronic Devices and Circuits, Electromagnetic Theory and Transmission Lines, Switching Theory and Logic Design, Control Systems, Computer Networks, Digital IC applications through VHDL, Digital Communications, Digital Logic Design, Microwave Engineering, Microelectronics and VLSI design and
M.Tech : Embedded System Concepts, HW/SW Co-design, CMOS IC Design, Low power VLSI design