

R G M COLLEGE OF ENGINEERING & TECHNOLOGY
(AUTONOMOUS)

Mr. YOVAN SNANAGAN PONSELVAN D

ASSISTANT PROFESSOR

Electronics and Communication Engineering

Date of Birth : 21-10-1985

Date of Joining : 27-10-2022

Experience : 12 years

Email : dyspece@gmail.com

Mobile : 8870882688



EDUCATION QUALIFICATIONS:

Examination passed	Division	% of marks	Year	Institution address & University Name
SSLC	FWD	81.6	2001	SSV HSS Mathapattanam, Tenkasi Dt, Tamil Nadu.
HSC (Intermediate)	FWD	84.6	2003	SSV HSS Mathapattanam, Tenkasi Dt, Tamil Nadu.
B.E (ECE)	FWD	74.0	2007	Sardar Raja College of Engineering, Tenkasi Dt, Tamil Nadu. Anna University, Chennai.
M.E (Applied Electronics)	FWD	7.28 CGPA	2011	Sardar Raja College of Engineering, Tenkasi Dt, Tamil Nadu. Anna University of Technology, Tirunelveli.
Ph.D	---	----	Registered	Pondicherry Central University

RESEARCH & PUBLICATIONS

- No. of Ph.Ds Guided/Guiding - Guided (-) Guiding (-)
- Text Books - Nil
- Book Chapter - Nil
- International Journals - 01
- International Conferences - Nil
- National Journals - 01
- Conferences/Workshops/Symposia's - 12 (Attended)
- Research Funding Projects - Nil
- Patents - Nil
- Consultancy Works - Ongoing (-) Completed (-)

MEMBERSHIPS & HONORARY ROLES

- LIFE MEMBER IN IETE, NEW DELHI (M235382)

WORKSHOPS, SYMPOSIA, SEMINARS, GUEST LECTURERS & CONFERENCES ORGANIZED:01

AWARDS & ACHIEVEMENTS:

- Best Paper Award In The National Conference On Recent Trends And Emerging Technologies In Electrical Systems On 18.03.2011, Presented the paper "Artifact Removal from EEG signal by TLMSFA"

COURSES HANDLED/HANDLING:

1. Digital Logic Design (DLD)
2. Digital Design using Verilog (DDV)
3. Microwind and LABVIEW (MWLV)
4. Linear and Digital Integrated Circuits and Applications (LDICA)