

R G M COLLEGE OF ENGINEERING & TECHNOLOGY

(AUTONOMOUS)

Mr. J Leela Mahendra Kumar

Assistant Professor,

Electronics and Communication Engineering

Date of Birth: 25-11-1985

Date of Joining: 01-06-2008

Experience: 11 Years 2 Months

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Qualifications: (Start with highest degree)

Examination passed	Class	% of marks	Year	University/Institution
SSC	FIRST	79.42	2002	Secondary School of Education, AP
Intermediate (MPC)	FIRST	64.2	2004	Board of Intermediate Education, Hyderabad
B.E (ECE)	FIRST	62.56	2008	JNTUH/ RGM ENGG., COLLEGE
M.Tech(DSCE)	FWD	79.54	2011	JNTUA/RGM ENGG., COLLEGE
Ph.D	-	-	-	-

RESEARCH & PUBLICATIONS

- Text Books -
- Book Chapter -
- International Journals - 05
- International Conferences - 01
- National Journals - 01
- Conferences/Workshops/Symposia's - 14
- Research Funding Projects - Ongoing (), Completed ()
- Patents -
- Consultancy Works - Ongoing(), Completed()

MEMBERSHIPS & HONARARY ROLES

- IAENG

Workshops, Symposia, Seminars, Guest lecturers & Conferences organized: 04

- Organized as coordinator for 3 day workshop for IoT (embedded system) by APSSDC from 11 to 13 Jan 2018.
- Organized as Co-coordinator for 15 day SDP workshop on Design & Simulation of Advanced Process Control Applications" from 18-30th March 2013.
- Organized as Co-coordinator for 24 hours Hackaton by SUMVN in 2018.
- Organized as coordinator for 5 day FDP Recent Trends in Internet of Things 4.0 and Machine learning from 11 to 15 August 2020.

Awards & Achievements: 01

- Received a certificate for one time for securing 100% pass result (2009)

Courses Handled/Handling:

- Switching Theory and Logic Design.
- Electronic Measurements and Instrumentation.
- Digital system Design.
- FPGA Architecture and Applications.
- Embedded-C & Verilog.

- Embedded-C.
- Digital IC Applications through VHDL.
- Sensors and signal Conditioning.
- Industrial Instrumentation.
- Telemetry and Telecontrol.
- Digital Logic Design.
- Computer Networks.
- Microwind and LabVIEW.